

09-05-00

GAA-2811 #16/CPA



Atty Docket No. SEL 123

CONTINUED PROSECUTION APPLICATION TRANSMITTAL LETTER

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:

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<p>PRIOR APPLICATION:</p> <p>Docket No. SEL 123</p> <p>Serial No.: 09/241,695</p> <p>Filed: February 2, 1999</p> <p>Examiner: S. Hu</p> <p>Art Unit: 2811</p> <p>Title: Semiconductor Device And Process For Producing The Same</p> <p>Applicant: Aikiharu MIYANAGA et al.</p>	<p style="text-align: right;">SEP 08 2000 TECHNOLOGY CENTER 2800</p> <p>"Express Mail" Mailing Label No. EL015962501</p> <p>Date of Deposit <u>September 1, 2000</u></p> <p>I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner for Patents, Washington, D.C. 20231</p> <p>Name <u>ARMANDO CHING</u> (typed or printed)</p> <p>Signature <u>Armando Ching</u></p>
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Box CPA
 Assistant Commissioner for Patents
 Washington, D.C. 20231

This is a request for filing a continuing application under the continuing prosecution procedure, 37 C.F.R. §1.53(d).

☒ please enter the Preliminary Amendment C filed herewith before calculating the fees and examining this CPA application.

I. Particulars Of Prior Application

A. Application Serial No. 09/241,695 filed February 2, 1999.

B. Title: Semiconductor Device And Process For Producing The Same

09/06/2000 CVORACHA 00000040 09241695

01 FC:131
 02 FC:103
 03 FC:102

690.00 OP
 522.00 OP
 312.00 OP

- C. Name of applicant(s) (as originally filed and last amended) and current correspondence address of applicant(s)

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Name: Akiharu MIYANAGA
Citizenship: Japanese
Residence and
Mailing Address: c/o Semiconductor Energy Laboratories, Co., Ltd., 398, Hase,
Atsugi-shi, Kanagawa-ken 243-0036 Japan

Name: Nobuo KUBO
Citizenship: Japanese
Residence and
Mailing Address: c/o Semiconductor Energy Laboratories, Co., Ltd., 398, Hase,
Atsugi-shi, Kanagawa-ken 243-0036 Japan

II. Inventorship Statement

- ☒ This application discloses and claims only subject matter disclosed in the prior application whose particulars are set out above and the inventor(s) in this application is(are) the same.

III. Declaration or oath

- ☒ This is a continuation or divisional application and no oath or declaration is required.

IV. Priority-35 U.S.C. 119

- ☒ Priority of application Serial No. 10-048673 filed on February 12, 1998 in Japan is claimed under 35 U.S.C. 119.

☒ the certified copy has been filed in 09/241,695

V. Relate Back-35 U.S.C. 120

- ☒ This is a continuing prosecution application of serial number 09/241,695 filed on February 2, 1999

VI. Assignment

- ☒ the prior application is assigned of record to Semiconductor Energy Laboratory Co., Ltd.

II. Power of Attorney

X The power of attorney in the prior application is to

Edward D. Manzo

X The power appears in the original papers in the prior application.

X The Power of Attorney is the same for the present application.

X Address all future communications to:

Edward D. Manzo

Cook, Alex, McFarron, Manzo, Cummings & Mehler, Ltd.

200 W. Adams Street, Suite 2850

Chicago, Illinois 60606

(312) 236-8500

VIII. Maintenance of Copendency of Prior Application

X A petition and fee has been filed to file a Notice of Appeal which was filed on August 7, 2000.

X No extension of time is believed to be necessary. If an extension of time is needed, a conditional extension of time is hereby being requested. Please charge our deposit account 50/1039 for any fee due.

IX. Fees

X The fees to be charged are to be based on the number of claims remaining as a result of the

the unentered amendment(s) after final filed under 37 CFR 1.116 in the prior application, which is(are) now repeated.

X the attached preliminary amendment

— A verified statement of small entity status has been filed in the parent application no. _____ for which priority is claimed under 35 USC §120.

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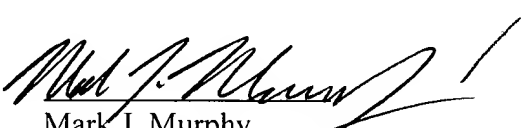
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Claims as Filed

	Number Filed		Number Extra	Rate	Fee
Total	49	-20	29	(small entity) x 9 (others) x 18	\$522.00
Independent	7	-3	4	(small entity) x 39 (others) x 78	\$312.00
Multiple Dependent	0	0	0	(small entity) x 130 (others) x 260	\$0.00
Basic Fee				(small entity) x 345 (others) x 690	\$690.00
Assignment					\$0.00
Total Fee					\$1524.00

- X The Commissioner is hereby authorized to charge any additional fees (except the issue fee) which may be required at any time during the prosecution of this application without specific authorization, or credit any over payment to Deposit Account No. 50/1039. A duplicate copy of this sheet is enclosed.
- X A check in the amount of \$1524.00 is enclosed to cover the filing fee and the recordation of the assignment, if any, transmitted herewith.

Respectfully submitted,



Mark J. Murphy
Registration No. 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS, MEHLER, LTD.
200 West Adams Street, Suite 2850
Chicago, Illinois 60606
(312) 236-8500



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Attorney Docket SEL 123

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)
Akiharū MIYANAGA et al.)
Serial No.: 09/241,695)
Filed: February 2, 1999)
For: Semiconductor Device And Process)
For Producing The Same)
Art Unit: 2811)
Examiner: S. Hu)

"Express Mail" Mailing Label No. EL613562567

Date of Deposit September 1, 2000

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Name ARMANDO CHIN
(typed or printed)

Signature Armando Chin

Assistant Commissioner for Patents
Washington, D.C. 20231

August 29, 2000

PRELIMINARY AMENDMENT C

Sir:

This Amendment is being filed with a Continuing Prosecution Application. Please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 2, 4, and 14.

Please amend claims 1, 3, 15, 18, 21, 24, and 27 as follows:

1. (Twice amended) A semiconductor device comprising a plurality of MOSFETs formed in a single semiconductor substrate,
each of the plurality of MOSFETs comprising: